

PATENT
81790.0300

Express Mail Label No. EV 325 217 514 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Takayuki KAWAGUCHI et al.

Serial No: Not assigned

Filed: September 25, 2003

For: Power Supply Voltage Lowering Circuit
Used in Semiconductor Device

Art Unit: Not assigned

Examiner: Not assigned

**TRANSMITTAL OF INFORMATION DISCLOSURE
STATEMENT**

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAN & HARTSON, L.L.P.

By: 

Anthony J. Orler

Registration No. 41,232

Attorney for Applicant(s)

Date: September 25, 2003

500 South Grand Avenue, Suite 1900
Los Angeles, California 90071
Telephone: 213-337-6700
Facsimile: 213-337-6701

